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<u>L37</u> L34 and l15	6	<u>L37</u>
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<u>L35</u> L34 and l12	101	<u>L35</u>
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<u>L31</u> l9 and l16	14	<u>L31</u>
<u>L30</u> l9 and l15	54	<u>L30</u>

<u>L29</u>	l9 and l14	98	<u>L29</u>
<u>L28</u>	l9 and l12	237	<u>L28</u>
<u>L27</u>	l1 and l17	0	<u>L27</u>
<u>L26</u>	l1 and l16	0	<u>L26</u>
<u>L25</u>	l1 and l15	0	<u>L25</u>
<u>L24</u>	l1 and l14	0	<u>L24</u>
<u>L23</u>	l1 and l12	1	<u>L23</u>
<u>L22</u>	l11 and l17	0	<u>L22</u>
<u>L21</u>	l11 and l16	0	<u>L21</u>
<u>L20</u>	l11 and l15	0	<u>L20</u>
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<u>L17</u>	(713/164)![CCLS]	305	<u>L17</u>
<u>L16</u>	(725/6)[CCLS]	67	<u>L16</u>
<u>L15</u>	(902/26)[CCLS]	171	<u>L15</u>
<u>L14</u>	(705/64)[CCLS]	608	<u>L14</u>
<u>L13</u>	(712/31-38)[CCLS]	1420	<u>L13</u>
<u>L12</u>	(712/31-38)![CCLS]	1420	<u>L12</u>
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<u>L11</u>	l9 and stack\$2 near55 register\$1 near4 (file or bank or group\$4 or plur\$7 or multiple)	409	<u>L11</u>
<u>L10</u>	L9 near45 l1	0	<u>L10</u>
<u>L9</u>	(security or permission\$1 or permit\$3 or protect\$5) near5 (rom\$1 or memor\$4 or ram\$1 or eeprom\$1 or eprom\$1 or storage\$1)	124849	<u>L9</u>
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<u>L8</u>	(712/2-300)[CCLS]	2278	<u>L8</u>
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<u>L7</u>	(712/2-300)![CCLS]	11885	<u>L7</u>
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<u>L5</u>	(free or user\$1) near6 (programming or programmable or programable or program\$3) near85 L1	1	<u>L5</u>
<u>L4</u>	(free or user\$1) near6 (programming or programmable or programable or program\$3) near85 L1	1	<u>L4</u>
<u>L3</u>	(free or user\$1) near6 (programming or programmable or programable or program\$3) and L1	30	<u>L3</u>
<u>L2</u>	(free or user\$1) near6 (programming or programmable or programable or program\$3) near45 L1	1	<u>L2</u>
<u>L1</u>	(register\$1) near4 (file or bank or group\$4 or plur\$7 or multiple) near35 (indicat\$5 or specif\$5) near8 (value\$ or content\$1 or data) near12 (read\$3 or stor\$3 or writ\$5 or written) near25 stack\$2	78	<u>L1</u>

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Malhi, S.D.S.; Chatterjee, P.K.; Bonfield, T.D.; Leiss, J.E.; Carter, D.E.; Pinizzotto, R.F.; Coleman,
[Electron Device Letters, IEEE](#)
Volume 5, Issue 10, Oct 1984 Page(s):428 - 429
[AbstractPlus](#) | Full Text: [PDF](#)(232 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 2. **A study of eddy-current effects in the structure of the westinghouse LCT coil**
Luton, J.; Shen, S.; McManamy, T.; Wilson, C.; Ellis, J.; Dresner, L.; Wintenberg, R.;
[Magnetics, IEEE Transactions on](#)
Volume 23, Issue 2, Mar 1987 Page(s):1497 - 1500
[AbstractPlus](#) | Full Text: [PDF](#)(432 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 3. **The Princeton Engine: a real-time video system simulator**
Chin, D.; Passe, J.; Bernard, F.S.; Taylor, H.; Knight, S.P.;
[Consumer Electronics, IEEE Transactions on](#)
Volume 34, Issue 2, May 1988 Page(s):285 - 297
Digital Object Identifier 10.1109/30.2943
[AbstractPlus](#) | Full Text: [PDF](#)(1032 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 4. **Evaluation of a three-dimensional memory cube system**
Bertin, C.L.; Perlman, D.J.; Shanken, S.N.;
[Components, Hybrids, and Manufacturing Technology, IEEE Transactions on](#) [see also [IEEE Trans](#)
[Packaging, and Manufacturing Technology, Part A, B, C](#)]
Volume 16, Issue 8, Dec. 1993 Page(s):1006 - 1011
Digital Object Identifier 10.1109/33.273703
[AbstractPlus](#) | Full Text: [PDF](#)(524 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 5. **Performance modeling of multiprocessor implementations of protocols**
Bjorkman, M.; Gunningberg, P.;
[Networking, IEEE/ACM Transactions on](#)
Volume 6, Issue 3, June 1998 Page(s):262 - 273
Digital Object Identifier 10.1109/90.700890
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(284 KB) [IEEE JNL](#)

[Rights and Permissions](#)**6. Protection scheme of a charging and discharging system for a 500 kJ capacitor bank**

Geun-Hie Rim; Chu-Hyun Chu; Young-Wook Choi; Youn-Sik Jin; Pavlov, E.P.;

[Magnetics, IEEE Transactions on](#)

Volume 37, Issue 1, Part 1, Jan. 2001 Page(s):389 - 393

Digital Object Identifier 10.1109/20.911861

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(296 KB) [IEEE JNL](#)[Rights and Permissions](#)**7. Triple high κ stacks (Al/sub 2/O/sub 3/HfO/sub 2/Al/sub 2/O/sub 3/) with high pres 2/ and D/sub 2/ annealing for SONOS type flash memory device applications**

Sanghun Jeon; Choi, S.; Park, H.; Hyunsang Hwang; Jung Hee Han; Hisun Chae; Soo Doo Chae;

Kyung Kim; Youn Seok Jeong; Yoondong Park; Sunare Seo; Jo Won Lee; Chung Woo Kim;

[Nanotechnology, 2004, 4th IEEE Conference on](#)

16-19 Aug. 2004 Page(s):53 - 55

Digital Object Identifier 10.1109/NANO.2004.1392247

[AbstractPlus](#) | Full Text: [PDF](#)(533 KB) [IEEE CNF](#)[Rights and Permissions](#)**8. A highly manufacturable low-k ALD-SiBN process for 60nm NAND flash devices and beyond**

Jin-Gyun Kim; Jae-Young Ahn; Hong-Suk Kim; Ju-Wan Lim; Chae-Ho Kim; Shu, H.; Hasebe, K.; S

Park; Hee-Seok Kim; Yu-Gyun Shin; U-In Chung; Joo-Tae Moon;

[Electron Devices Meeting, 2004, IEDM Technical Digest, IEEE International](#)

13-15 Dec. 2004 Page(s):1063 - 1066

Digital Object Identifier 10.1109/IEDM.2004.1419380

[AbstractPlus](#) | Full Text: [PDF](#)(1874 KB) [IEEE CNF](#)[Rights and Permissions](#)**9. Proceedings of the 34th European Solid-State Device Research Conference**[Solid-State Device Research conference, 2004, ESSDERC 2004, Proceeding of the 34th European](#)

21-23 Sept. 2004

Digital Object Identifier 10.1109/ESSDER.2004.1356461

Full Text: [PDF](#)(159 KB) [IEEE CNF](#)[Rights and Permissions](#)**10. Security vulnerabilities - from data analysis to protection mechanisms**

Iyer, R.K.; Shuo Chen; Jun Xu; Kalbarczyk, Z.;

[Object-Oriented Real-Time Dependable Systems, 2003, Proceedings, Ninth IEEE International Wo](#)

1-3 Oct. 2003 Page(s):331 - 338

Digital Object Identifier 10.1109/WORDS.2003.1267548

[AbstractPlus](#) | Full Text: [PDF](#)(306 KB) [IEEE CNF](#)[Rights and Permissions](#)**11. Simulation of nanofloating gate memory with high-k stacked dielectrics**

Govoreanu, B.; Blomme, P.; Van Houdt, J.; De Meyer, K.;

[Simulation of Semiconductor Processes and Devices, 2003, SISPAD 2003, International Conferen](#)

3-5 Sept. 2003 Page(s):299 - 302









[AbstractPlus](#) | Full Text: [PDF](#)(537 KB) [IEEE CNF](#)[Rights and Permissions](#)**12. Plasma-induced charging in two bit per cell SONOS memories**

Roizin, Y.; Gutman, M.; Yosefi, R.; Alfassi, S.; Aloni, E.;

[Plasma- and Process-Induced Damage, 2003, 8th International Symposium](#)

24-25 April 2003 Page(s):61 - 64

[AbstractPlus](#) | Full Text: [PDF](#)(551 KB) [IEEE CNF](#)[Rights and Permissions](#)

-  **13. Solid-state modulator R&D for JLC**
 Akemoto, M.; Chin, Y.H.; Sakamoto, Y.;
Pulsed Power Plasma Science, 2001. PPS-2001. Digest of Technical Papers
 Volume 2, 17-22 June 2001 Page(s):1059 - 1062 vol.2
 Digital Object Identifier 10.1109/PPS.2001.1001726
[AbstractPlus](#) | Full Text: [PDF](#)(350 KB) [IEEE CNF](#)
[Rights and Permissions](#)
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 Matekovits, L.; Vecchi, G.; Dassano, G.; Orefice, M.;
Antennas and Propagation Society International Symposium, 2001. IEEE
 Volume 2, 8-13 July 2001 Page(s):568 - 571 vol.2
 Digital Object Identifier 10.1109/APS.2001.959787
[AbstractPlus](#) | Full Text: [PDF](#)(160 KB) [IEEE CNF](#)
[Rights and Permissions](#)
-  **15. RAD: a compile-time solution to buffer overflow attacks**
 Tzi-Cker Chiueh; Fu-Hau Hsu;
Distributed Computing Systems, 2001. 21st International Conference on
 16-19 April 2001 Page(s):409 - 417
 Digital Object Identifier 10.1109/ICDSC.2001.918971
[AbstractPlus](#) | Full Text: [PDF](#)(712 KB) [IEEE CNF](#)
[Rights and Permissions](#)
-  **16. Low-cost, high density non volatile memory for military and space applications**
 Castrucci, D.P.;
Nonvolatile Memory Technology Conference, 1998. 1998 Proceedings. Seventh Biennial IEEE
 22-24 June 1998 Page(s):100
 Digital Object Identifier 10.1109/NVMT.1998.723230
[AbstractPlus](#) | Full Text: [PDF](#)(48 KB) [IEEE CNF](#)
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 Zuberi, K.M.; Shin, K.G.;
Real-Time Technology and Applications Symposium, 1996. Proceedings. 1996 IEEE
 10-12 June 1996 Page(s):241 - 249
 Digital Object Identifier 10.1109/RTTAS.1996.509541
[AbstractPlus](#) | Full Text: [PDF](#)(912 KB) [IEEE CNF](#)
[Rights and Permissions](#)
-  **18. Development of high density memory IC package by stacking IC chips**
 Nakanishi, H.; Maruyama, T.; Miyata, K.; Ishio, T.; Sota, Y.; Narai, A.; Fukunaga, S.; Toyosawa, K.;
Electronic Components and Technology Conference, 1995. Proceedings. 45th
 21-24 May 1995 Page(s):634 - 640
 Digital Object Identifier 10.1109/ECTC.1995.515349
[AbstractPlus](#) | Full Text: [PDF](#)(1280 KB) [IEEE CNF](#)
[Rights and Permissions](#)
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 Chin, D.; Passe, J.; Bernard, F.; Taylor, H.; Knight, S.;
Consumer Electronics, 1988. Digest of Technical Papers. ICCE... IEEE 1988 International Conferer
 8-10 June 1988 Page(s):144 - 145
 Digital Object Identifier 10.1109/ICCE.1988.10740
[AbstractPlus](#) | Full Text: [PDF](#)(188 KB) [IEEE CNF](#)
[Rights and Permissions](#)
-  **20. A versatile stacked storage capacitor on FLOTOX cell for megabit NVRAM applications**
 Yamauchi, Y.; Ishihara, H.; Tanaka, K.; Sakiyama, K.; Miyake, R.;
Electron Devices Meeting, 1989. Technical Digest. International

3-6 Dec. 1989 Page(s):595 - 598

Digital Object Identifier 10.1109/IEDM.1989.74351

[AbstractPlus](#) | Full Text: [PDF](#)(232 KB) [IEEE CNF](#)

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21. Evaluation of a 3D memory cube system

Bertin, C.L.; Perlman, D.J.; Shanken, S.N.;

[Electronic Components and Technology Conference, 1993. Proceedings., 43rd](#)

1-4 June 1993 Page(s):12 - 15

Digital Object Identifier 10.1109/ECTC.1993.346858

[AbstractPlus](#) | Full Text: [PDF](#)(312 KB) [IEEE CNF](#)

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22. A Gbit-scale DRAM stacked capacitor technology with ECR MOCVD SrTiO₃ and RIE pattern nodes

Lesacherre, P.-Y.; Yamamichi, S.; Yamaguchi, H.; Takemura, K.; Watanabe, H.; Tokashiki, K.; Sat Yoshida, M.; Ohnishi, S.; Nakajima, K.; Shibahara, K.; Miyasaka, Y.; Ono, H.;

[Electron Devices Meeting, 1994. Technical Digest., International](#)

11-14 Dec. 1994 Page(s):831 - 834

Digital Object Identifier 10.1109/IEDM.1994.383296

[AbstractPlus](#) | Full Text: [PDF](#)(368 KB) [IEEE CNF](#)

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23. Fuel cell power systems for submersibles

DeRonck, H.J.;

[OCEANS '94. 'Oceans Engineering for Today's Technology and Tomorrow's Preservation.' Proceed](#)

Volume 3, 13-16 Sept. 1994 Page(s):III/449 - III/452 vol.3

Digital Object Identifier 10.1109/OCEANS.1994.364240

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